國立臺北科技大學一百零七學年第二學期電機系博士班資格考試試題範本說明

- 一. 本系博士班資格考試試題為 A4 格式之版面。
- 二. 提供之試題範本自第1頁起提供 A4 格式之版面共 4頁, 若有不足請自行加頁。
- 三. 本範本以 Office 之 Word 文書應用軟體製作,命題委員至少須輸入之資料共四項, 各項簡要說明如下:(前三項請依範本上之原字型與字型大小輸入,前二項已代為 執行合併列印套稿,請確認組別名稱與考試科目。謝謝您!)
 - (一)【考試科目名稱】 => [依所附檔案內考試科目名稱完整輸入取代]
 - (二) ⇒ [請依試題題數輸入取代並增加必要之配分與各項特殊規定]

注意事項:

- 1. 本試題共【1】題,配分共100分。
- 2. 請按順序標明題號作答,不必抄題。
- 3. 全部答案均須答在試卷答案欄內,否則不予計分。
- 4. 考試時間:二小時。

(三)

試題本文 => [請輸入題號與試題內容並完成排版與列印]

範本版面說明

試題本文之外方格線,係以單格表格並以隱藏格線方 式設計,請在格線內命題,不要超出格線外;若有圖 片,亦請於列印後黏貼於規劃版面內。謝謝!

- 四. 命題版面達 A4 共 2 頁(含)以上時,請修改範本第 1 頁之 第一頁 共一頁 為 第一 頁 共二頁;若頁數更多,請類推修改增加之。
- 五. 本範本檔案及考試科目名稱檔案,將由本系以隨身碟提供命題委員,請命題委員在規劃版面內命題,並以 A4 紙張列印出試題繳交,隨身碟亦請交給本系。本系將直接列印後隨即製版,不再作其他處理,若有圖片請自行黏貼於妥當之版面位置。

國立臺北科技大學

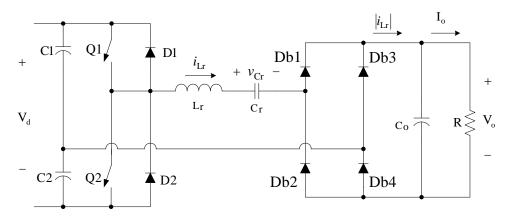
一百零七學年第二學期電機系博士班資格考試

切換式電源設計 試題

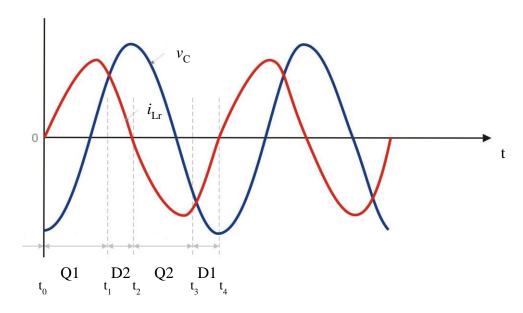
第一頁 共2頁

- 本試題共【4】題,配分共100分。
 請按順序標明題號作答,不必抄題。
 全部答案均須答在試卷答案欄內,否則不予計分。
- 1. If a forward converter has the following parameters: $V_d=48 \text{ V}$, $R=10 \Omega$, L=0.4 mH, Lm=5 mH, C=100 μ F, f_s=35 kHz, N₁/N₂=1.5, N₁/N₃=1, and D=0.4. (a) Determine the output voltage, the maximum and minimum currents in L, and the output voltage ripple. (b) Determine the peak current in the transformer primary winding. Verify that the magnetizing current is reset to zero during each switching period. Assume all components are ideal. 20%
- 2. Please illustrate and find the output current of buck converter while operating in boundary current conduction mode (BCM) and explain how to determine the operation mode whether continuous current conduction mode (CCM) or discontinuous current conduction mode (DCM). 20%
- 3. Design a converter to produce an output voltage of 36 V from a 3.3 V source. The output current is 0.1 A and the switching frequency is 100 kHz. Design for an output ripple voltage of 2 percent and let the current variation in L_m be 40 percent of the average current. Include ESR when choosing a capacitor. Assume for this problem that the ESR is related to the capacitor value by $r_c=10^{-5}/C$. 20%

4. A series resonant converter is shown as below.



- (a) Please specify which switches including transistors and diodes can be switched with ZCS or ZVS and associated time points from t_0 to t_4 in the case of $f_s > f_0$. The reference waveforms are illustrated as below. 10%
- (b) Please derive the voltage ratio of V_o/V_d in the case of $f_s > f_0$. 20%
- (c) While V_d =100V, C_r =0.08 μ F, L_r =30 μ H, R=10 Ω and the switching frequency f_s =120kHz, please determine the output voltage depending on the above analysis. 10%



Reference waveforms in case of $f_s > f_0$